

TRANSMITTAL

Electronic Version v1.1

Stylesheet Version v1.1.0

Title of Invention	STRUCTURE AND METHOD OF MAKING STRAINED CHANNEL CMOS TRANSISTORS HAVING LATTICE-MISMATCHED EPITAXIAL EXTENSION AND SOURCE AND DRAIN REGIONS	
Application Number :		
Date :		
First Named Applicant:	Huajie Chen	
Confirmation Number:		
Attorney Docket Number:	FIS920030198US1	
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Submitted By: H. Daniel Schnurmann Registered Number: 35791	Elec. Sign. H. Daniel Schnurmann /s/	Sign. Capacity Agent

Documents being submitted:	Files
us-fee-sheet	FIS920030198US1-usfees.xml us-fee-sheet.xsl us-fee-sheet.dtd
us-assignment	FIS920030198US1-usassn.xml us-assignment.xsl us-assignment.dtd FIS920030198US1Assignment1.tif FIS920030198US1Assignment2.tif
us-request	FIS920030198US1-usreq.xml us-request.dtd us-request.xsl
us-declaration	FIS920030198US1Declaration2.tif
us-declaration	FIS920030198US1Declaration3.tif
application-body	FIS920030198-trans.xml us-application-body.xsl application-body.dtd wipo.ent mathml2.dtd mathml2-qname-1.mod isoamsa.ent isoamsb.ent isoamsc.ent isoamsn.ent isoamso.ent isoamsr.ent isogr3.ent isomfr.ent isomopf.ent isomscr.ent isotech.ent isobox.ent isocyr1.ent isocyr2.ent isodia.ent isolat1.ent isolat2.ent isonum.ent isopub.ent mmlextra.ent mmlalias.ent soextblk.dtd FIS920030198US1Drawing1.tif FIS920030198US1Drawing2.tif FIS920030198US1Drawing3.tif FIS920030198US1Drawing4.tif FIS920030198US1Drawing5.tif FIS920030198US1Drawing6.tif FIS920030198US1Drawing7.tif FIS920030198US1Declaration1.tif
us-declaration	

Comments

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